



Essential technology, done right™

## Digital Design and Verification intern for Storage product

### ABOUT MARVELL

At Marvell, we believe that infrastructure powers progress. That execution is as essential as innovation. That better collaboration builds better technology. Trusted by the world's leading technology companies for 25 years, we move, store, process and secure the world's data with semiconductor solutions designed for our customers' current needs and future ambitions. Through a process of deep collaboration and transparency, we're ultimately changing the way tomorrow's enterprise, cloud, automotive, and carrier architectures transform—for the better.

The data infrastructure that our customers build has never been more critical to our global economy. It's what's keeping the world connected, businesses running, and information flowing. If you're ready to excel, innovate, and truly enjoy your work, apply now for the position detailed below.

### ABOUT THE ROLE

- **Digital Design role**
  - Defining Digital Design strategy for specific designs based on design specifications & standards.
  - Defining design solutions and design Specification for specific designs.
  - Develop designs using Verilog, System Verilog.
  - Work on ASIC design flows such as Synthesis, RTL/Netlist Quality tools (DC, Spyglass, Concert, Conformal LEC, etc), DFT/BIST integration,...
  - Work with Design Verification team to test the design including defining testcases, debugging issues.
- **Digital Design Verification role**
  - Develop design verification testplan to describe test scenarios following design specifications & standards.
  - Develop testbench and tests using Verilog, SystemVerilog, UVM, and C language.
  - Analyze functional and code coverage result. Enhance the coverage by randomization.
  - Work with Digital Design team to test the design including defining testcases, debugging issues.



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## ABOUT YOU

- BS/MS in Computer Science, Electrical Engineering or or related fields, or the equivalent work experience that provides knowledge and exposure to theories, principles and concepts. Prefer: GPA: 7.0+
- Have basic knowledge of proگرامing languages as C/C++, etc.
- Have basic knowledge of HW design languages as Verilog/System-Verilog/VHDL, etc.
- Have basic knowledge of Linux environment
- Strong problem-solving skill
- Excellent communication skill
- Good in English skills

## BENEFITS

- Competitive compensation and benefit package
- Exciting internal events
- Hybrid work model

## HOW TO APPLY

Please send your CV to [ungo@marvell.com](mailto:ungo@marvell.com)

Or

[https://marvell.wd1.myworkdayjobs.com/MarvellCareers/job/Ho-Chi-Minh/Digital-Design-and-Verification-intern-for-Storage-product\\_2203517](https://marvell.wd1.myworkdayjobs.com/MarvellCareers/job/Ho-Chi-Minh/Digital-Design-and-Verification-intern-for-Storage-product_2203517)

## WHERE TO FIND US

**Website:** [www.marvell.com](http://www.marvell.com)